

## **Defense Microelectronics Activity**

Defense Microelectronics Activity, [www.dmea.osd.mil](http://www.dmea.osd.mil)

Solicitation Dates\*:

**	Proposals Pre-release	Proposals Accepted	Proposals Due (Closed)	Award Date	Amount
<b>Phase I</b>	April 12, 2008	May 19 2008	June 18 2008		Up to \$100,000
<b>Phase II</b>					Up to \$750,000***

\* dates are estimates and **subject to change** by the agencies

\*\* If you want to be notified when a solicitation becomes available, please sign for listserv [sbirlist@listserv.dodsbir.net](mailto:sbirlist@listserv.dodsbir.net)

\*\*\*Base program and options may total a maximum of \$2,500,000

Note: Only Companies that have a Phase I award are eligible to apply for Phase II

**Proposal submission info** – electronic, <http://www.dodsbir.net/submission/>

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## DMEA SBIR 07.2 Topic Index

- DMEA07-1 High-Throughput Experimentation Physical Vapor Deposition (PVD) Chamber for Accelerated Microelectronics Materials Research and Development
- DMEA07-2 In-Line Characterization System for Advanced High K Dielectric / Metal Gate CMOS Transistor Stack for the Development of High Speed, Low Power Microelectronics
- DMEA07-3 Ultra Low-Power Miniaturized Flexible Radio Optimized for Long-Term Battery Operation